IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):

Johan Torsner, et al.

Group Art Unit:

Unknown

Serial No:

Unknown

Examiner:

Unknown

For:

Synchronizing Method and Apparatus Using Error Detection of Sequence

Numbers to Avoid Synchronizing Failure

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Date of Deposit: December 2, 2004

Dear Sir:

INFORMATION DISCLOSURE STATEMENT

In accordance with the duty of disclosure as set forth in 37 C.F.R. § 1.56, Applicant(s) hereby submit the following information in conformance with 37 C.F.R. §§ 1.97 and 1.98. Copies of the cited references were previously transmitted to the USPTO by the international searching authority.

To assist the Examiner, the documents are listed on the attached form PTO 1449. It is respectfully requested that the Examiner initial a copy of this form and return it to the undersigned.

Respectfully submitted,

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In place of PTO-1449 Form	10-1449 PATENT AND TRADEMARK OFFICE		Complete if Known			
, 5				Application Number	Not Yet Known	_
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Filing Date	December 2, 2004	
				Applicant(s)	Johan Torsner, et al.	
(use as many sheets as necessary)				Art Unit	Not Yet Known	_
ľ				Examiner Name	Not Yet Known	
CHEET	1	OF	1	Attorney Docket Number	P16904-US1	- -

	U. S. PATENT DOCUMENTS					
Examiner's Cite Initials No.		Document Number	Publication Date	Name of Patentee or Applicant of Cited Document		
	AA	2003007490 A1	01/09/2003	Yi, S.J. et al		
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FOREIGN PATENT DOCUMENTS						
Examiner's Initials	Cite No.	Foreign Patent Document (Country Code - Number - Kind)	Publication Date	Patentee or Applicant of Cited Document	Translation Y/N	
	AB	EP 1276293 A2	01/15/2003	LG Electronics Inc	<u> </u>	
	AC	WO 0156249 A1	08/02/2001	Telefonaktiebolaget LM Ericsson (publ)		
	AD	JP 2001 83860 A	06/30/2000	Fujitsu Ltd		
			 			
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OTHER PRIOR ART					
Examiner's Initials	Cite Include name of the author (in CAPITAL LETTERS), title of the article, title of the item, date, page No. issue number(s), publisher, city/country where published				
	ĀĒ	SA DA COSTA, C. ET AL, Sequence synchronization in the ATM adaption layer, Proceedings of the Mediterranean Electrotechnical Conference, 1991, 22-24 May 1991, pages 505-508, vol. 1. page 505, column 3, line 1, page 507, column 2, line 26			
	AF	Patent Abstracts of Japan, vol. 2000, no 09, October 12, 2000			
	AG	Swedish patent Office, International Search Report for PCT/SE/03/00866, dated August 19, 2003.			

Examiner	 Date	
Signature	Considered	